

Sangamitrareddy Katamreddy

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EDUCATION

Master of Science, Electrical Engineering,
George Mason University, Fairfax, VA

Expected Graduation May 2015

- Member, [Cryptographic Engineering Research Group \(CERG\)](#), Volgenau School of Engineering

Bachelor of Technology (B.Tech), Electronics and Communication Engineering
Vellore Institute of Technology, India

May 2013

- Cumulative GPA – 8.16/10

TECHNICAL SKILLS

Programmable Hardware

Xilinx FPGA

Programming Languages

C, C++, Java, Matlab

Scripting Language

Python

Hardware Languages

VHDL, Assembly coding

Tools

Wire Shark, ModelSim, Xilinx ISE, Altera Quartus,
Code Composer Studio, Keil uVision3, GnuPG, Cryptool.

AREAS OF INTEREST

Side channel Analysis, Hardware implementations of Cryptographic Algorithms, Digital Signal Processing.

PROJECT WORK

- **An Analysis of BD+ and Other Major Schemes of DRM (Analytical Project)** Fall 2013
The analysis of the digital right copy protection techniques and their respective attacks is done. The project is done under the guidance of Dr. Kris Gaj.
- **Electromagnetic Analysis** Spring 2014
The overview of the electromagnetic over years is studied. The types of electromagnetic analysis and the dependency of the characteristics on many factors is studied. The project is done under the guidance of Dr. Jens Peter Kaps.